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IN THE CLAIMS:

1. (Previously Presented) A plasma display panel (hereinafter referred to as "PDP") driving method for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells to accumulate a charge in each discharge cell;

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image; and

10 a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells,

wherein the set-up pulse applied during the set-up step has a staircase waveform that rises in at least two steps and a voltage jump for a first-step rise of the staircase waveform being no less than $V_f - 70V$ but no greater than V_p when V_f is a discharge starting voltage.

2. (Original) The PDP driving method of Claim 1, wherein:

15 the staircase waveform for the set-up pulse is a staircase waveform that falls in at least two steps.

3. (Previously Presented) The PDP driving method of Claim 1, wherein an average rate of change of voltage from the end of a first-step rise to the end of a second step rise in the waveform for the set-up pulse is no less than $1V/\mu s$ but no greater than $9V/\mu s$.

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4. (Previously Presented) The PDP driving method of Claim 1, wherein a voltage jump for the first-step rise in the waveform for the set-up pulse is no less than $V_f - 70V$ but no greater than V_f when V_f is a discharge starting voltage.

5. (Previously Presented) A PDP driving method for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to accumulate a charge in each discharge cell;

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image; and

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells,

wherein the set-up pulse applied during the set-up step has a staircase waveform that falls in at least two steps and a ratio of a first-step voltage Fall (V_1) to a peak voltage (V_{st}) being no less than 0.05:1, and no more than 0.8:1, and a ratio of (a) a time (t_p) from an end of the first-step fall to a start of a second-step fall to (b) pulse width (t_w) being no less than 0.05:1 and no more than 0.6:1.

6. (Previously Presented) The PDP driving method of Claim 1, wherein the staircase waveform for the set-up pulse is generated by adding at least two pulses and applying the resulting pulse.

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7. (Previously Presented) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a write step for applying a write pulse to selected discharge cells of the plurality
5 of discharge cells to write an image; and

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells;

wherein the write pulse applied in the write step has a staircase waveform that falls in at least two steps and a peak voltage of no less than 70V but no more than 100V, and a
10 pulse width of no less than 0.5 μ s but no more than 2.00 μ s.

8. (Original) The PDP driving method of Claim 7, wherein the staircase waveform of the write pulse applied in the write step rises in at least two steps.

9. (Previously Presented) The PDP driving method of Claim 7, wherein a voltage jump for a second-step rise in the waveform for the write pulse is no less than 10V but no greater
15 than 100V.

10. (Previously Presented) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a write step for applying a write pulse to selected discharge cells of the plurality
20 of discharge cells to write an image; and

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells;

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wherein the write pulse applied in the write step has a staircase waveform that rises in at least two steps and a peak voltage of no less than 70V but no more than 100V, and a pulse width of no less than 0.5 μ s but no more than 2.0 μ s.

11. (Previously Presented) The PDP driving method of Claim 7 and Claim 10,

5 wherein the staircase waveform of the write pulse in the write step is generated by adding at least two pulses and applying the resulting pulse.

12-15. (Cancelled)

16. (Previously Presented) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the

10 following steps to perform image display:

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image; and

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells;

15 wherein the sustain pulses applied in the sustain step have a staircase waveform that falls in at least two steps and a maximum voltage V that is no less than V_f but no greater than $V_f + 150V$, when V_f is a discharge starting voltage.

17. (Previously Presented) The PDP driving method of Claim 16, wherein the maximum voltage for the sustain pulses is no less than V_f , but no greater than $V_f + 150V$, when

20 V_f is a discharge starting voltage.

18-23. (Cancelled)

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24. (Original) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a write step for applying a write pulse to selected discharge cells of the plurality
5 of discharge cells to write an image; and

a discharge sustain step for applying at least one sustain pulse to each of the plurality of discharge cells after the write step to perform a sustain discharge in the discharge cells corresponding to the written image;

wherein the waveform for the sustain pulses applied in the sustain step is set such
10 that a voltage applied when a discharge current is highest is higher than a voltage at the discharge starting point.

25. (Original) The PDP driving method of Claim 24, wherein:

the rise portion of the waveform for the sustain pulses is in the form of a linear or approximately linear slope that has a constant gradient.

15 26. (Previously Presented) The PDP driving method of Claim 16, wherein the waveform for each sustain pulse is such that a phase for a variation in the discharge current is set earlier than a phase for variation in a voltage applied to the discharge cells during a period in the pulse between a point where a discharge current starts and a point where the discharge current reaches a peak value.

20 27. (Previously Presented) The PDP driving method of Claim 16, wherein the waveform for the sustain pulses in the sustain step is generated by adding at least two pulses and applying the resulting pulse.

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28. (Previously Presented) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a write step for applying a write pulse to selected discharge cells of the plurality
5 of discharge cells to write an image; and

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells;

wherein only a first sustain pulse applied in the sustain step has a staircase waveform in which at least one of the rising and falling portions is performed in at least two
10 steps.

29. (Original) The PDP driving method of Claim 28, wherein the first sustain pulse applies a maximum voltage for at least $0.1\mu\text{s}$ longer than for each of second and subsequent sustain pulses.

30. (Previously Presented) The PDP driving method of Claim 28, wherein the first
15 sustain pulse applies the maximum voltage for at least $0.02\mu\text{s}$ but for no longer than 90% of a pulse width PW.

31. (Previously Presented) The PDP driving method of Claim 16, wherein the sustain step, the waveform for the sustain pulses is generated by adding at least two pulses and applying the resulting pulse.

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32. (Previously Presented) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

5 a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image;

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells; and

an erase step for applying an erase pulse to each discharge cell after the discharge sustain step to erase the image,

10 wherein the erase pulse applied in the erase step has a staircase waveform that rises in at least two steps and a voltage in a first-step rise being no less than $V_f - 50V$ but no greater than $V_f + 30V$, when V_f is a discharge starting voltage.

33. (Cancelled)

34. (Previously Presented) The PDP driving method of Claim 32, wherein a voltage
15 in a first-step rise in the waveform for the erase pulse is no less than V_f but no greater than $V_f + 100V$, when V_f is a discharge starting voltage.

35. (Original) A PDP driving method, for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

20 a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image;

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a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells; and

an erase step for applying an erase pulse to each discharge cell after the discharge sustain step to erase the image,

5 wherein the waveform for the erase pulse applied in the erase step is a staircase waveform that rises in at least two steps; and

a time between a rise of the erase pulse and a point at which a maximum voltage ceases to be applied is no less than $T_{df} - 0.1\mu s$ but no greater than $T_{df} + 0.1\mu s$, when T_{df} is a discharge delay time for the pulse.

10 36. (Original) The PDP driving method of Claim 35, wherein a first-step rise of the waveform for the erase pulse no less than V_f but no greater than $V_f + 100V$, when V_f is a discharge starting voltage.

37. (Currently Amended) The PDP driving method of Claim 32, wherein the waveform for the erase pulse in the erase [[104]] step is generated by adding at least two pulses
15 and applying the resulting pulse.

38. (Original) A PDP driving method for a PDP in which a plurality of discharge cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells
20 to accumulate a charge in each discharge cell;

a write step for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image;

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a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells, and

an erase step for applying an erase pulse to each discharge cell after the discharge sustain step to erase the image,

5 wherein the waveforms for the set-up pulse applied in the set-up step, the write pulse applied in the write step, the first sustain pulse applied in the sustain step and the erase pulse applied in the erase step is a staircase waveform in which at least one of the rising and falling portions is performed in at least two steps.

39. (Original) A PDP driving method for a PDP in which a plurality of discharge
10 cells are arranged between a pair of substrates, the PDP driving method repeating the following steps to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells to accumulate a charge in each discharge cell;

a write step for applying a write pulse to selected discharge cells of the plurality
15 of discharge cells to write an image;

a discharge sustain step for applying at least one sustain pulse to each discharge cell after the write step to perform a sustain discharge in the selected discharge cells , and

an erase step for applying an erase pulse to each discharge cell after the discharge sustain step to erase an image,

20 wherein the waveforms for the set-up pulse applied in the set-up step, the write pulse applied in the write step, the sustain pulses applied in the sustain step and the erase pulse

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applied in the erase step is a staircase waveform in which at least one of the rising and falling portions is performed in at least two steps.

40. (Previously Presented) The PDP driving method of Claim 32, wherein a discharge gas is enclosed in each of the discharge cells in the PDP at a pressure of 800 to 4 000
5 torr.

41. (Original) The PDP driving method of Claim 40, wherein an inert gas mixture including helium, neon, xenon and argon is used as the discharge gas.

42. (Previously Presented) The PDP driving method of Claim 40, wherein the discharge gas is a mixture containing not more than 55% xenon, not more than 0.5% argon and
10 less than 55% helium.

43. (Original) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of substrates; and

a driving apparatus including:

15 a set-up unit for applying a set-up pulse to each of the plurality of discharge cells to accumulate a charge in each discharge cell;

a write unit for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image; and

20 a discharge sustain unit for applying at least one sustain pulse to each of the plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image,

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wherein the set-up unit includes a pulse adding means for generating the set-up pulse by adding at least two pulses.

44. (Original) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of
5 substrates; and

a driving apparatus including:

a write unit for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image; and

a discharge sustain unit for applying at least one sustain pulse to each of the
10 plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image,

wherein the write unit includes a pulse adding means for generating the write pulse by adding at least two pulses.

45. (Previously Presented) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of
15 substrates; and

a driving apparatus including:

a write unit for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image; and

a discharge sustain unit for applying at least one sustain pulse to each of the
20 plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image,

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wherein the discharge sustain unit includes an adding means for generating the each sustain pulse by adding at least two pulses.

46. (Original) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of
5 substrates; and

a driving apparatus including:

a write unit for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image;

a discharge sustain unit for applying at least one sustain pulse to each of the
10 plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image; and

an erase unit for applying an erase pulse to each of the plurality of discharge cells to erase an image,

wherein the erase unit includes a pulse adding means for generating the erase
15 pulse by adding at least two pulses.

47. (Original) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of substrates; and

a driving apparatus including:

a write unit for applying a write pulse to selected discharge cells of the plurality
20 of discharge cells to write an image; and

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a discharge sustain unit for applying at least one sustain pulse to each of the plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image,

wherein the waveform for the write pulse applied by the write unit and the sustain
5 pulses applied by the discharge sustain unit have staircase waveforms in which at least one of the rising and falling portions has at least two steps.

48. (Original) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of substrates; and

10 a driving apparatus including:

a set-up unit for applying a set-up pulse to each of the plurality of discharge cells to accumulate a charge in each discharge cell;

a write unit for applying a write pulse to selected discharge cells of the plurality of discharge cells to write an image;

15 a discharge sustain unit for applying at least one sustain pulse to each of the plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image; and

an erase unit for applying an erase pulse to each of the plurality of discharge cells to erase an image,

20 wherein the set-up pulse applied by the set-up unit, the write pulse applied by the write unit, the first sustain pulse applied by the discharge sustain unit and the erase pulse applied

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by the erase unit have staircase waveforms in which at least one of the rising and falling portions is performed in at least two steps.

49. (Original) A PDP display apparatus, comprising:

a PDP in which a plurality of discharge cells are arranged between a pair of

5 substrates; and

a driving apparatus including:

a set-up unit for applying a set-up pulse to each of the plurality of discharge cells

to accumulate a charge in each discharge cell;

a write unit for applying a write pulse to selected discharge cells of the plurality

10 of discharge cells to write an image;

a discharge sustain unit for applying at least one sustain pulse to each of the plurality of discharge cells to perform a sustain discharge in the discharge cells corresponding to the written image; and

an erase unit for applying an erase pulse to each of the plurality of discharge cells

15 to erase an image,

wherein the set-up pulse applied by the set-up unit, the write pulse applied by the write unit, and the sustain pulses applied by the discharge sustain unit and the erase pulse applied by the erase unit have staircase waveforms in which at least one of the rising and falling portions is performed in at least two steps.

20 50. (Previously Presented) The PDP display apparatus of Claim 48, wherein the discharge gas is enclosed in each of the discharge cells in the PDP at a pressure of 800 to 4 000 torr.

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51. (New) A plasma display panel driving method for a plasma display panel in which a plurality of discharge cells are arranged, each discharge cell having a pair of a first electrode and a second electrode, the plasma display panel driving method repeating the following steps to perform image display:

5 a set-up step for applying a set-up pulse to each of the plurality of discharge cells, wherein the set-up pulse applied in the set-up step has a waveform that rises at an average voltage change rate of no less than 1 V/ μ s and no greater than 9 V/ μ s, and that starts to fall at a rate greater than the average voltage change rate at a time of the rising.

52. (New) The plasma display panel driving method according to Claim 51,
10 wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than 5.25 V/ μ s and no greater than 9 V/ μ s.

53. (New) The plasma display panel driving method according to Claim 51, wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than 4.2 V/ μ s and no greater than 5.25 V/ μ s.

15 54. (New) The plasma display panel driving method according to Claim 51, wherein the set-up pulse applied in the set-up step rises at the average voltage change rate of no less than 3.5 V/ μ s and no greater than 4.2 V/ μ s.

55. (New) The plasma display panel driving method according to Claim 51, wherein the set-up pulse applied in the set-up step rises at the average voltage
20 change rate of no less than 2.625 V/ μ s and no greater than 3.5 V/ μ s.

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56. (New) The plasma display panel driving method according to Claim 51,
wherein the set-up pulse applied in the set-up step rises at the average voltage
change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

57. (New) The plasma display panel driving method according to Claim 51,
5 wherein the set-up pulse applied in the set-up step rises at the average voltage
change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

58. (New) A plasma display panel driving method for a plasma display panel in
which a plurality of discharge cells are arranged, each discharge cell having a pair of a first
electrode and a second electrode, the plasma display panel driving method repeating the
10 following steps to perform image display:

a set-up step for applying a set-up pulse to each of the plurality of discharge cells;
and

a write step for applying a write pulse to selected discharge cells of the plurality
of discharge cells based on image data input,

15 wherein in the set-up step, the set-up pulse is applied via the first electrodes and
has a waveform that rises at an average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater
than $9 \text{ V}/\mu\text{s}$ and that starts to fall at a rate greater than the average voltage change rate at a time
of the rising,

wherein in the write step, the write pulse is applied to the selected discharge cells
20 via the first electrodes,

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59. (New) The plasma display panel driving method according to Claim 58,
wherein the set-up pulse applied in the set-up step rises at the average voltage
change rate of no less than $5.25 \text{ V}/\mu\text{s}$ and no greater than $9 \text{ V}/\mu\text{s}$.

60. (New) The plasma display panel driving method according to Claim 58,
5 wherein the set-up pulse applied in the set-up step rises at the average voltage
change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

61. (New) The plasma display panel driving method according to Claim 58,
wherein the set-up pulse applied in the set-up step rises at the average voltage
change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

10 62. (New) The plasma display panel driving method according to Claim 58,
wherein the set-up pulse applied in the setup step rises at the average voltage
change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

63. (New) The plasma display panel driving method according to Claim 58,
wherein the set-up pulse applied in the set-up step rises at the average voltage
15 change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

64. (New) The plasma display panel driving method according to Claim 58,
wherein the set-up pulse applied in the set-up step rises at the average voltage
change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

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65. (New) A plasma display apparatus comprising:

a plasma display panel that includes a plurality of pairs of a first electrode and a second electrode, and

a plurality of discharge cells, each discharge cell having one of the pairs of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by repeating a set-up period of applying a set-up pulse to the discharge cells, wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse having a waveform that rises at an average voltage change rate of no less than 1 V/ μ s and no greater than V/ μ s, and that starts to fall at a rate greater than the average voltage change rate at a time of the rising.

66. (New) The plasma display apparatus according to Claim 65,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than 5.25 V/ μ s and no greater than 9 V/ μ s.

67. (New) The plasma display apparatus according to Claim 65,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than 4.2 V/ μ s and no greater than 5.25 V/ μ s.

68. (New) The plasma display apparatus according to Claim 65,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than 3.5 V/ μ s and no greater than 4.2 V/ μ s.

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69. (New) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

5 70. (New) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

10 71. (New) The plasma display apparatus according to Claim 65,
wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.

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72. (New) A plasma display apparatus comprising:

a plasma display panel that includes a plurality of pairs of a first electrode and a second electrode; and

a plurality of discharge cells, each discharge cell having one of the pairs of the first electrode and the second electrode; and

a driving circuit operable to drive the plasma display panel by repeating a set-up period of applying a set-up pulse to the discharge cells, and

a write period of applying a write pulse to selected discharge cells of the plurality of discharge cells based on image data input,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse via the first electrodes, the set-up pulse having a waveform that rises at an average voltage change rate of no less than 1 V/ μ s and no greater than 9 V/ μ s and that starts to fall at a rate greater than the average voltage change rate at a time of the rising, and

wherein the driving circuit is operable to apply, during the write period, the write pulse to the selected discharge cells via the first electrodes.

73. (New) The plasma display apparatus according to Claim 72,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than 5.25 V/ μ s and no greater than 9 V/ μ s.

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74. (New) The plasma display apparatus according to Claim 72,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $4.2 \text{ V}/\mu\text{s}$ and no greater than $5.25 \text{ V}/\mu\text{s}$.

5 75. (New) The plasma display apparatus according to Claim 72,

wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $3.5 \text{ V}/\mu\text{s}$ and no greater than $4.2 \text{ V}/\mu\text{s}$.

76. (New) The plasma display apparatus according to Claim 72,

10 wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.625 \text{ V}/\mu\text{s}$ and no greater than $3.5 \text{ V}/\mu\text{s}$.

77. (New) The plasma display apparatus according to Claim 72,

15 wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $2.1 \text{ V}/\mu\text{s}$ and no greater than $2.625 \text{ V}/\mu\text{s}$.

78. (New) The plasma display apparatus according to Claim 72,

20 wherein the driving circuit is operable to apply, during the set-up period, the set-up pulse that rises at the average voltage change rate of no less than $1 \text{ V}/\mu\text{s}$ and no greater than $2.1 \text{ V}/\mu\text{s}$.